

SET5010-1 Inputs

reserved ----- ○
 PLC ready ----- ●
 Testing ----- ○
 No Test ----- ●
 Calibration ----- ○
 reserved ----- ○
 BELD Alarm ----- ●
 LAM Alarm ----- ○

ACL7125-1 Inputs

reserved ----- ●
 reserved ----- ●
 reserved ----- ●
 reserved ----- ●
 reserved ----- ●
 reserved ----- ●
 reserved ----- ●
 reserved ----- ●

SET5010-1 Outputs

0ff |
 1 | Watch dog ----- ○
 2 | reserved ----- ○
 3 | reserved ----- ○
 4 | Measure-PC ON ----- ●
 5 | Shift Register ON ----- ○
 6 | reserved wired ----- ○
 7 | reserved wired ----- ○
 8 | reserved wired ----- ○

ACL7125-1 Outputs

0ff |
 1 | Reject LOD ----- ●
 2 | Reject ROD ----- ○
 3 | Reject LID ----- ○
 4 | Reject RID ----- ○
 5 | Reject LL1 ----- ○
 6 | Reject RL1 ----- ○
 7 | Reject LL2 ----- ○
 8 | Reject RL2 ----- ○